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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Levine et al.

Serial No.: 10/675,872

Filed: September 30, 2003

For: Method and Apparatus for  
Counting Instruction and Memory  
Location Ranges

35525

PATENT TRADEMARK OFFICE  
CUSTOMER NUMBER

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Group Art Unit: 2192

Examiner: Vo, Ted T.

Attorney Docket No.: AUS920030487US1

Certificate of Mailing Under 37 C.F.R. § 1.8(a)

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By: Louise Fay  
Louise Fay

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Commissioner for Patents  
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Sir:

ENCLOSED HERewith:

- Supplemental Information Disclosure Statement;
- Form PTO-1449;
- References AG-AI; and
- Our return postcard

No fees are believed to be necessary. If, however, any fees are required, I authorize the Commissioner to charge these fees which may be required to Deposit Account No. 09-0447. No extension of time is believed to be necessary. If, however, an extension of time is required, the extension is requested, and I authorize the Commissioner to charge any fees for this extension to Deposit Account No. 09-0447.

Respectfully submitted,

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

**Levine et al.**

Serial No: **10/675,872**

Filed: **September 30, 2003**

Title: **Method and Apparatus for Counting  
Instruction and Memory Location Ranges**

Group Art Unit: **2192**

Intellectual Property  
Law Department  
International Business  
Machines Corporation  
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**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. 1.56.

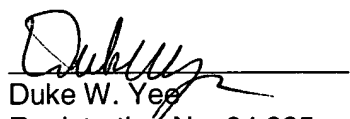
While this Supplemental Information Disclosure Statement may be "material" pursuant to 37 C.F.R. 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

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In the event a fee is required in connection with the enclosed Supplemental Information Disclosure Statement, the Commissioner of Patents and Trademarks is authorized to charge Deposit Account No. 09-0447 for the necessary amount.

Respectfully submitted,

  
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**Complete if Known**

Application Number	10/675,872
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	Vo, Ted T.
Art Unit	2192
Attorney Docket No.	AUS920030487US1

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

[illegible]

## FOREIGN PATENT DOCUMENTS

Examiner Initial		Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

## NON PATENT LITERATURE

<b>Examiner Initial</b>	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	
	<b>AG</b>	Wikipedia "JavaServer Pages" downloaded 1/24/06 <a href="http://en.wikipedia.org/wiki/JavaServer_Pages">http://en.wikipedia.org/wiki/JavaServer_Pages</a>
	<b>AH</b>	"Hardware Cycle Based memory Residency", IBM, May 22, 2003, ip.com, IPCOM000012728D, pp. 1-2
	<b>AI</b>	HYDE, "The Art of Assembly Language", 2001, Linux Edition, pp. 247-248, retrieved Mar. 1, 2005 from <a href="http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html">http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html</a>

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